

Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company



CD5N60/CD5N60F VDMOS Power Transistor

TO-220/TO220F

Absolute maximum ratings (Tamb=25°C)

Paramete	r	Symbol	Rating value	Unit
drain current (continuo	ous)	I _D	5.0	Α
drain source voltage		V_{GS}	±30	V
drain current (pulsed))	I _{DM}	20	Α
thermal resistance junction case)		R _{èJC}	1.04	°C/W
dissipated namer	Ta=25°C	P _{tot}	3.13	W
dissipated power	Tc=25°C	P _{tot}	120	W
junction temperature		Tj	-55 to 150	°C
storage temperature		Tstg	-55 to 150	°C

Electrical characteristics (Tamb=25°C)

Donomotor	Cumbal	Test conditions	Value			l locit
Parameter	Symbol	rest conditions	MIN	NOR	MAX	Unit
D-S voltage	V_{DS}	$V_{GS}=0V, I_{D}=250\mu A$	600			V
D-S on resistance	R _{ON}	V _{GS} =10V, I _D =2.5A		1.57	2.0	Ω
gate threshold voltage	V_{th}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2		4	V
S-D leakage current	I _{DSS}	V _{DS} =600V, V _{GS} =0V			10	μΑ
G-S leakage current	I _{GSS}	V _{GS} = <u>+</u> 30V			±100	nA
transconductance	gm	V _{DS} =50V, I _D =2.5A		4.0		S
input capacitance	C _{IN}	V _{GS} =0V, V _{DS} =25V, f=1.0mHz		560	730	pF

Disclaimer

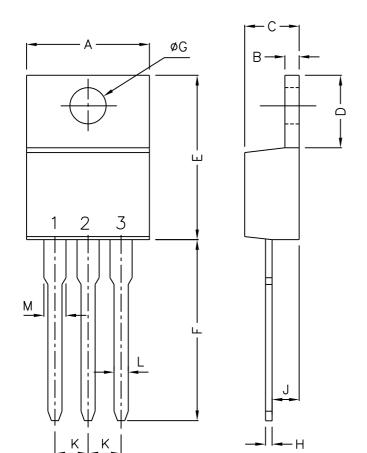
The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices o incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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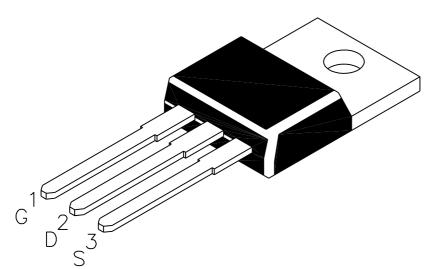
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PACKAGE TO-220



DIM	MIN	TYP.	MAX
А			10.7
В			1.4
\bigcirc			4.8
			6.9
EJ			16.5
L	12.5		
G		3.81	
I			0.4
つ		2.67	
K		2.51	
L			1.2
М		1.27	

ALL DIMENSIONS ARE IN mm

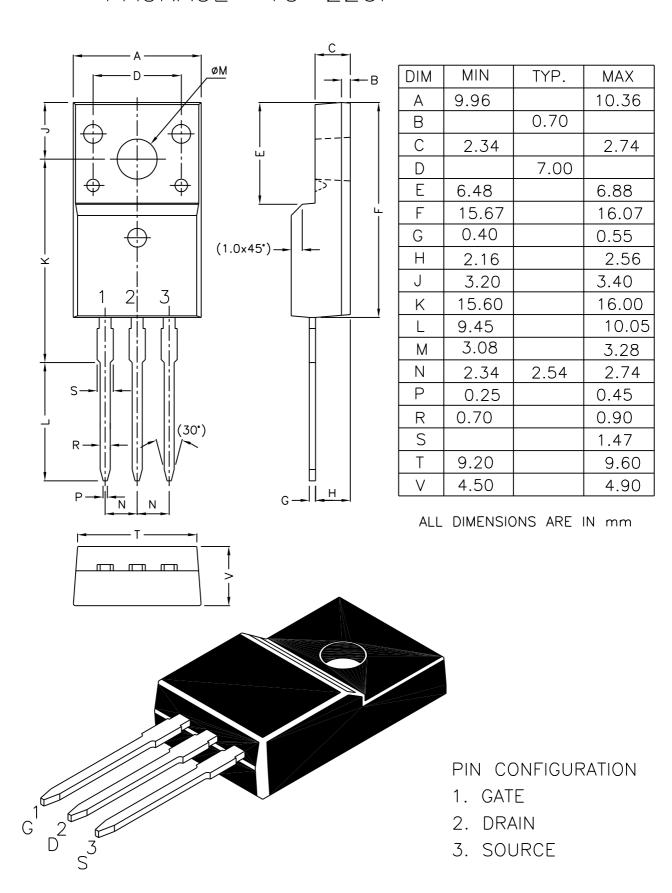


PIN CONFIGURATION

- 1. GATE
- 2. DRAIN
- 3. SOURCE

CD5N60 _F Rev_2 280110E

PACKAGE TO-220F



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