



PIC16F627A/628A

PIC16F627A/628A Rev. A Silicon Errata

The PIC16F627A/628A (Rev. A) parts you have received conform functionally to the Device Data Sheet (DS40044A), except for the anomalies described below.

This Errata applies to all PIC16F627A and PIC16F628A devices.

All problems listed here will be addressed in future revisions of the PIC16F627A/628A silicon.

1. PIC16F627A/628A silicon Rev. A3. Accessing of the data EEPROM memory in Low Voltage Programming (LVP) mode.
 - The data EEPROM memory cannot be accessed when programming in LVP mode.
 - Run-time reading and writing of the data EEPROM memory functions correctly.
 - The FLASH program memory and the configuration bits can be accessed properly in LVP mode.
2. PIC16F627A/628A silicon Rev. A2, A3 and A4. FLASH program memory can only be programmed with a VDD of 4.5V - 5.5V.
 - In future revisions of the PIC16F627A/628A silicon, the FLASH program memory will be able to be programmed with a VDD of 2.0V - 5.5V.
 - In all current and future revisions of the PIC16F627A/628A silicon, the FLASH program memory can only be erased using the Bulk Erase command. The Bulk Erase function requires a VDD of 4.5V - 5.5V.

Clarifications/Corrections to the Data Sheet:

In the Device Data Sheet (DS40044A), the following clarifications and corrections should be noted.

- None

PIC16F627A/628A

APPENDIX A: REVISION HISTORY

Rev. A Document (2/12/03)

First revision of this document.

Rev. B Document (3/26/03)

Added 4.5V - 5.5V VDD programming requirement on Rev. A2, A3 & A4 silicon.

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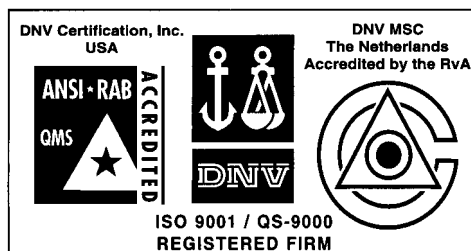
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