

Switchmode Power Transistor

MJE13005

Technical Data

Typical Applications : These devices are designed for high voltage , high speed power switching inductive circuits where fall time is critical . They are particularly suited for 115 V and 220 V SWITCHMODE applications such as Switching Regulator's , Inverters , Motor Controls , Solenoid / Relay drivers and Deflection circuits.

Specification Features :

- ☞ Switchmode series NPN Silicon Power Transistor
- ☞ 4 Amp / 400 V device in TO-220AB package
- ☞ 75 Watts device
- ☞ VCEO (sus) 400 V
- ☞ 700 V Blocking capability

Symbol	Parameters / Conditions	Ratings
Maximum Ratings :		
$V_{CEO(SUS)}$	Collector- Emitter Voltage	400 Vdc
V_{CEV}	Collector- Emitter Voltage	700 Vdc
V_{EBO}	Emitter Base Voltage	9 Vdc
I_C	Collector Current – Continuous	4 Adc
I_{CM}	Peak : Pulse width = 5 ms , Duty Cycle $\leq$ 10 %	8 Adc
I_B	Base Current – Continuous	2 Adc
I_{BM}	Peak : Pulse width = 5 ms , Duty Cycle $\leq$ 10 %	4 Adc
I_E	Emitter Current – Continuous	6 Adc
I_{EM}	Peak : Pulse width = 5 ms , Duty Cycle $\leq$ 10 %	12 Adc



Thermal Characteristics :		
R_{thjc}	Thermal resistance junction to case	1.67 $^{\circ}\text{C}/\text{W}$
R_{thjA}	Thermal resistance junction to ambient	62.5 $^{\circ}\text{C}/\text{W}$
T_L	Maximum Lead Temperature for Soldering Purpose : 1/8" from Case for 5 sec	275 $^{\circ}\text{C}$
P_D	Total Power Dissipation @ $T_a = 25^{\circ}\text{C}$ Derate above 25 $^{\circ}\text{C}$	2 Watta 16 mW / $^{\circ}\text{C}$
P_D	Total Power Dissipation @ $T_c = 25^{\circ}\text{C}$ Derate above 25 $^{\circ}\text{C}$	75 Watta 600 mW / $^{\circ}\text{C}$
T_j & T_{stg}	Operating and Storage Junction Temperature Range	-65 $^{\circ}\text{C}$+ 150 $^{\circ}\text{C}$

ELECTRICAL CHARACTERISTICS :

[$T_c = 25^{\circ}\text{C}$ unless otherwise noted]

Characteristic	Symbol	Min	Typ	Max	Unit
Off Characteristics : [Pulse Test : Pulse width = 300 ns , Duty Cycle = 2 %]					
Collector – Emitter Sustaining Voltage [$I_c = 10\text{ mA}$, $I_B = 0$]	$V_{CEO(sus)}$	400			Vdc
Collector Cutoff Current [$V_{CEV} = \text{Rated Value}$, $V_{BE(off)} = 1.5\text{ Vdc}$] [$V_{CEV} = \text{Rated Value}$, $V_{BE(off)} = 1.5\text{ Vdc}$, $T_c = 100^{\circ}\text{C}$]	I_{CEV}			1 5	mAdc
Emitter Cutoff Current [$V_{EB} = 9\text{ Vdc}$, $I_c = 0$]	I_{EBO}			1	mAdc
On Characteristics : [Pulse Test : Pulse width = 300 ns , Duty Cycle = 2 %]					
DC Current Gain [$I_c = 1\text{ Adc}$, $V_{CE} = 5\text{ Vdc}$] [$I_c = 2\text{ Adc}$, $V_{CE} = 5\text{ Vdc}$]	h_{FE}	10 8		60 40	
Collector-Emitter Saturation Voltage [$I_c = 1\text{ Adc}$, $I_B = 0.2\text{ Adc}$] [$I_c = 2\text{ Adc}$, $I_B = 0.5\text{ Adc}$]	$V_{CE(sat)}$			0.5 0.6	Vdc

[$I_C = 4 \text{ A dc}$, $I_B = 1.0 \text{ A dc}$] [$I_C = 2 \text{ A dc}$, $I_B = 0.5 \text{ A dc}$, $T_c = 100^\circ\text{C}$]				1 1	
Base-Emitter Saturation Voltage [$I_C = 1 \text{ A dc}$, $I_B = 0.2 \text{ A dc}$] [$I_C = 2 \text{ A dc}$, $I_B = 0.5 \text{ A dc}$] [$I_C = 2 \text{ A dc}$, $I_B = 0.5 \text{ A dc}$, $T_c = 100^\circ\text{C}$]	$V_{BE(sat)}$			1.2 1.6 1.5	Vdc
Dynamic Characteristics :					
Current Gain – Bandwidth Product [$I_C = 500 \text{ mAdc}$, $V_{CE}=10 \text{ Vdc}$, $f=1 \text{ MHz}$]	f_T	4			MHz
Output Capacitance [$V_{CB}= 10 \text{ Vdc}$, $I_E = 0$, $f = 0.1 \text{ MHz}$]	C_{ob}		65		pF

Switching Characteristics :			
Resistive Load :		Typ	Max
Delay Time ; t _d	(V _{CC} =125 V dc , I _c =2A , I _{B1} = I _{B2} = 0.4 A , tp=25 ns , Duty Cycle ≤ 1 %)	0.025 ns	0.1 ns
Rise Time ; t _r		0.3 ns	0.7 ns
Storage Time ; t _s		1.7 ns	4.0 ns
Fall Time ; t _f		0.4 ns	0.9 ns
Inductive Load ; Clamped :		Typ	Max
Voltage Storage Time ; t _{sv}	(Vclamp =300 V dc , I _c =2A , I _{B1} = 0.4 A , V _{BE(off)} = 5 V dc , T _c = 100 °C)	0.9 ns	4.0 ns
Crossover Time ; t _c		0.32 ns	0.9 ns
Fall Time ; t _{fi}		0.16 ns	--