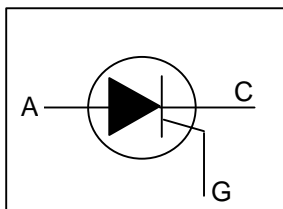
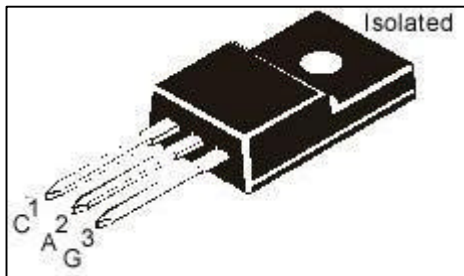


THYRISTORS

BT151X



TO-220FP Fully Isolated Plastic Package

For use in Applications Requiring High Bidirectional Blocking Voltage Capability and high Thermal Cycling Performance. Typical Applications include Motor Control, Industrial and Domestic Lighting, Heating and Static Switching.

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITION	VALUE		UNIT
		BT151X-	500	650	
Repetitive Peak Off State Voltage	V_{DRM}, V_{RRM}		*500	*650	V
Average On State Current	$I_T (AV)$	half sine wave, $T_{hs} \leq 87^\circ C$	5.7		A
RMS On State Current	$I_T (RMS)$	all conduction angles	9.0		A
Non Repetitive Peak On State Current	I_{TSM}	half sine wave, $T_J = 25^\circ C$ prior to surge $t = 10ms$ $t = 8.3ms$	100 110		A A
I^2t for Fusing	I^2t	$t = 10ms$	50		A^2s
Repetitive Rate Of Rise of On State Current After Triggering	di_T/dt	$I_{TM} = 20A, I_G = 50mA,$ $di_G/dt = 50mA/\mu s$	50		$A/\mu s$
Peak Gate Current	I_{GM}		2.0		A
Peak Gate Voltage	V_{GM}		5.0		V
Peak Reverse Gate Voltage	V_{RGM}		5.0		V
Peak Gate Power	P_{GM}		5.0		W
Average Gate Power	$P_{G (AV)}$	Over any 20ms period	0.5		W
Storage Temperature	T_{stg}		- 40 to +150		$^\circ C$
Operating Junction Temperature	T_J		125		$^\circ C$

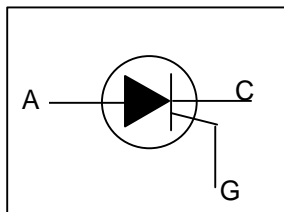
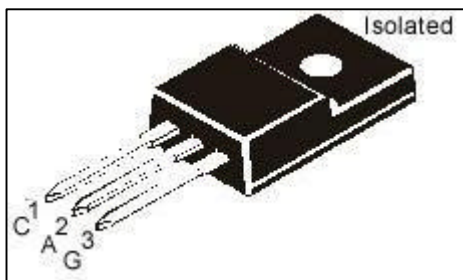
ISOLATION LIMITING VALUE and CHARACTERISTIC ($T_{hs} = 25^\circ C$ unless specified otherwise)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
R.M.S Isolation Voltage from all three terminals to external heatsink	V_{ISOL}	$f = 50-60$ Hz; sinusoidal waveform; R.H. $\leq 65\%$; clean and dustfree			2500	V
Capacitance from T2 to external heatsink	C_{ISOL}	$f = 1MHz$		10		pF

THERMAL RESISTANCE

Junction to Heatsink	$R_{th (j-hs)}$	with heatsink compound without heatsink compound	4.5 max 6.5 max	K/W K/W
Junction to Ambient	$R_{th (j-a)}$	in free air	55 typ	K/W

*Although not recommended, off state voltage upto 800V may be applied without damage, but the thyristor may switch to the on state. The rate of rise of current should not exceed 15A/ms



ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless specified otherwise)

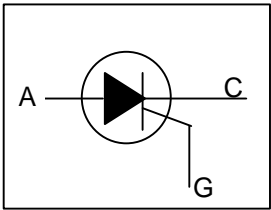
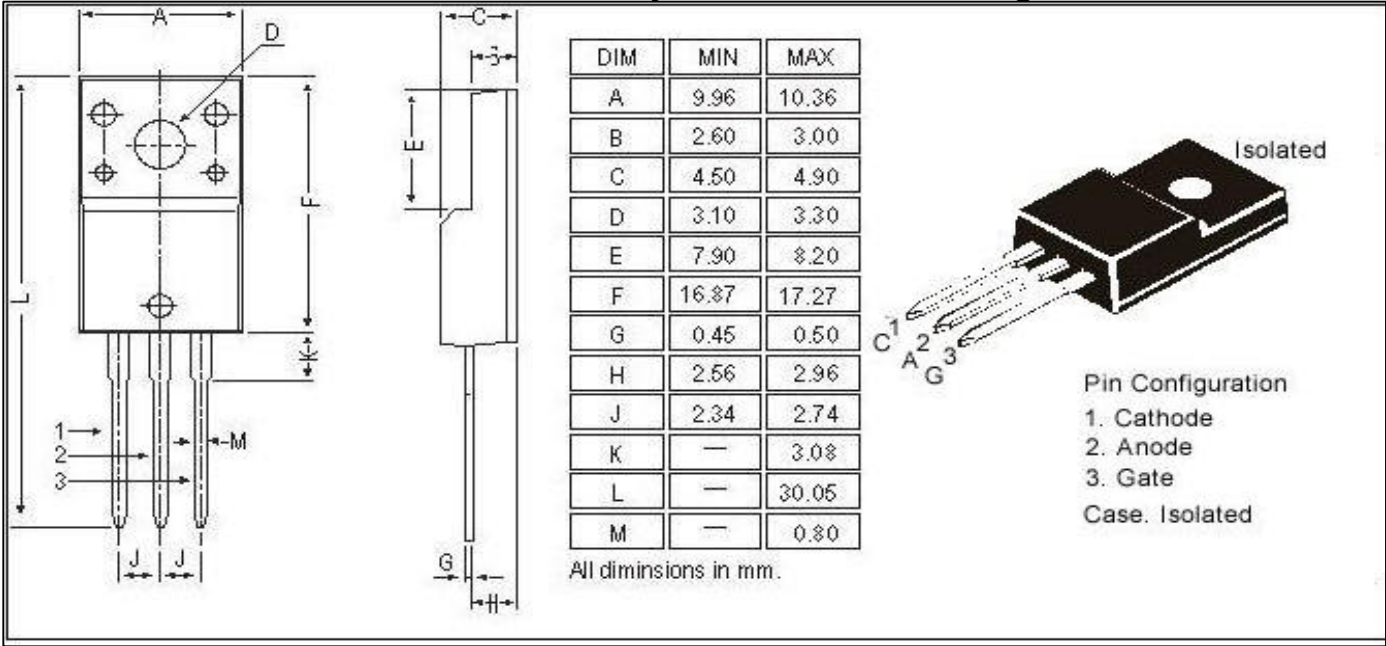
PARAMETER	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Gate Trigger Current	I_{GT}	$V_D=12\text{V}$, $I_T=0.1\text{A}$		15	mA
Latching Current	I_L	$V_D=12\text{V}$, $I_{GT}=0.1\text{A}$		40	mA
Holding Current	I_H	$V_D=12\text{V}$, $I_{GT}=0.1\text{A}$		20	mA
On State Voltage	V_T	$I_T=23\text{A}$		1.75	V
Gate Trigger Voltage	V_{GT}	$V_D=12\text{V}$, $I_T=0.1\text{A}$ $V_D=V_{DRM}(\text{max})$, $I_T=0.1\text{A}$, $T_J=125^\circ\text{C}$	0.25	1.5	V
Off State Leakage Current	I_D, I_R	$V_D=V_{DRM}(\text{max})$, $V_R=V_{RRM}(\text{max})$ $T_J=125^\circ\text{C}$		0.5	mA

DYNAMIC CHARACTERISTICS

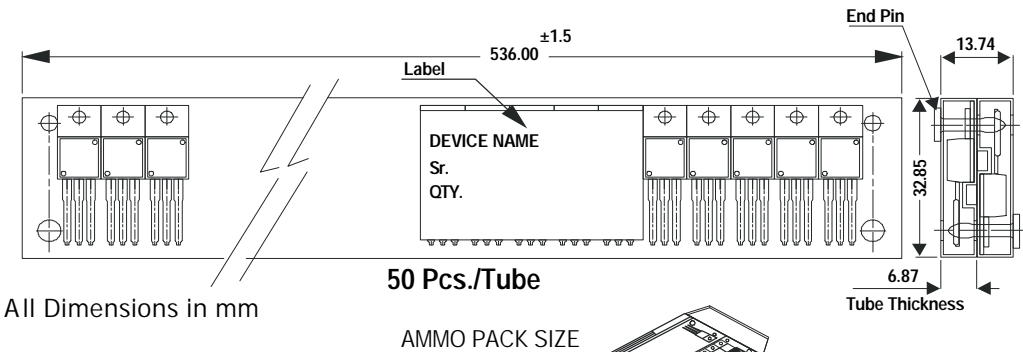
PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Critical Rate of Rise of Off State Voltage	dV_D/dt	$V_{DM}=67\%$ $V_{DRM}=\text{max}$, $T_J=125^\circ\text{C}$, exponential waveform gate open circuit $R_{GK}=100\Omega$	50 200			V/ μs V/ μs
Gate Controlled Turn On time	t_{gt}	$I_{TM}=40\text{A}$, $V_D=V_{DRM}(\text{max})$, $I_G=0.1\text{A}$, $dI_G/dt=5\text{A}/\mu\text{s}$		2.0		μs
Circuit Commutated Turn Off time	t_q	$V_{DM}=67\%$ $V_{DRM}(\text{max})$, $T_J=125^\circ\text{C}$, $I_{TM}=20\text{A}$, $V_R=25\text{V}$, $dI_{TM}/dt=30\text{A}/\mu\text{s}$, $dV_D/dt=50\text{V}/\mu\text{s}$, $R_{GK}=100\Omega$		70		μs

Marking	BT151X-500 CDXX BT151X - 500	BT151X-650 CDXX BT151X - 650
XX=Date Code		

TO-220FP Fully Isolated Plastic Package



TO-220 Tube Packing



Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-220 /FP	200 pcs/polybag	396 gm/200 pcs	3" x 7.5" x 7.5"	1.0K	17" x 15" x 13.5"	16.0K	36 kgs
	50 pcs/tube	120 gm/50 pcs	3.5" x 3.7" x 21.5"	1.0K	19" x 19" x 19"	10.0K	29 kgs

**TO-220FP Fully Isolated
Plastic Package****Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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CDIL is a registered Trademark of
Continental Device India Limited

C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-2579 6150 Fax + 91-11-2579 9569, 2579 5290
e-mail sales@cdil.com www.cdil.com www.cdilsemi.com