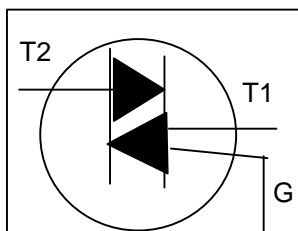
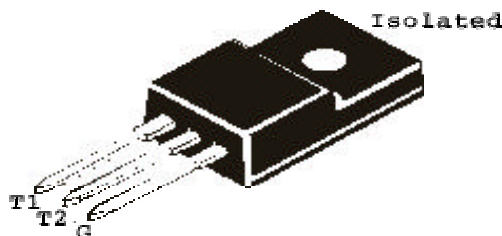


TRIAC
BT136X
TO-220FP Fully Isolated Plastic Package


For use in high bidirectional transient and blocking voltage applications, and for high thermal cycling performance. Typical Applications include Motor Control, Industrial and Domestic Lighting, Heating and Static Switching.

ABSOLUTE MAXIMUM RATINGS

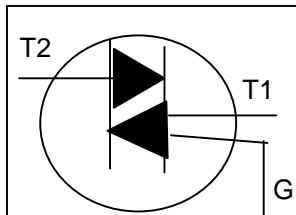
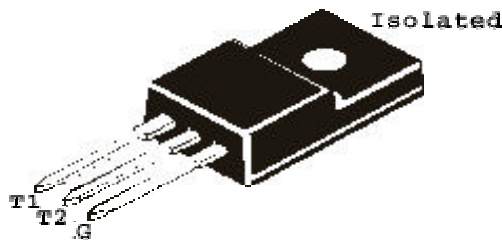
PARAMETER	SYMBOL	TEST CONDITION	VALUE	UNIT
Repetitive Peak Off State Voltage	$*V_{DRM}$		600	V
RMS on State Current	$I_T (RMS)$	Full sine wave, $T_{mb} \leq 92^\circ C$	4.0	A
Non Repetitive Peak on State Current	I_{TSM}	Full sine wave, $T_J = 25^\circ C$ prior to Surge		
		$t = 20ms$	25	A
		$t = 16.7ms$	27	A
I^2t for Fusing	I^2t	$t = 10ms$	3.1	A^2s
Repetitive Rate of Rise of On State Current after Triggering	di_T/dt	$I_{TM} = 6A$, $I_G = 0.2A$, $di_G/dt = 0.2A/\mu s$		
		T2+ G+	50	$A/\mu s$
		T2+ G-	50	$A/\mu s$
		T2- G-	50	$A/\mu s$
		T2- G+	10	$A/\mu s$
Peak Gate Current	I_{GM}		2.0	A
Peak Gate Voltage	V_{GM}		5.0	V
Peak Gate Power	P_{GM}		5.0	W
Average Gate Power	$P_{G(AV)}$	Over any 20ms period	0.5	W
Storage Temperature	T_{stg}		- 40 to +150	$^\circ C$
Operating Junction Temperature	T_J		125	$^\circ C$

THERMAL RESISTANCE

Junction to Heatsink	$R_{th(j-hs)}$	full or half cycle with heatsink compound	5.5 max	K/W
		full or half cycle without heatsink compound	7.2 max	K/W
Junction to Ambient	$R_{th(j-a)}$	in free air	55 typ	K/W

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ C$ unless specified otherwise)

PARAMETER	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Gate Trigger Current	I_{GT}	$V_D = 12V$, $I_T = 0.1A$			
		T2+ G+		35	mA
		T2+ G-		35	mA
		T2- G-		35	mA
		T2- G+		70	mA

**ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless specified otherwise)**

PARAMETER	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Latching Current	I_L	$V_D=12\text{V}$, $I_{GT}=0.1\text{A}$ T2+ G+ T2+ G- T2- G- T2- G+		20 30 20 30	mA mA mA mA
Holding Current	I_H	$V_D=12\text{V}$, $I_{GT}=0.1\text{A}$		15	mA
On State Voltage	V_T	$I_T=5\text{A}$		1.7	V
Gate Trigger Voltage	V_{GT}	$V_D=12\text{V}$, $I_T=0.1\text{A}$ $V_D=400\text{V}$, $I_T=0.1\text{A}$, $T_J=125^\circ\text{C}$	0.25	1.5	V V
Off State Leakage Current	I_D	$V_D=\text{max}$, $V_{DRM}=\text{max}$, $T_J=125^\circ\text{C}$		0.5	mA

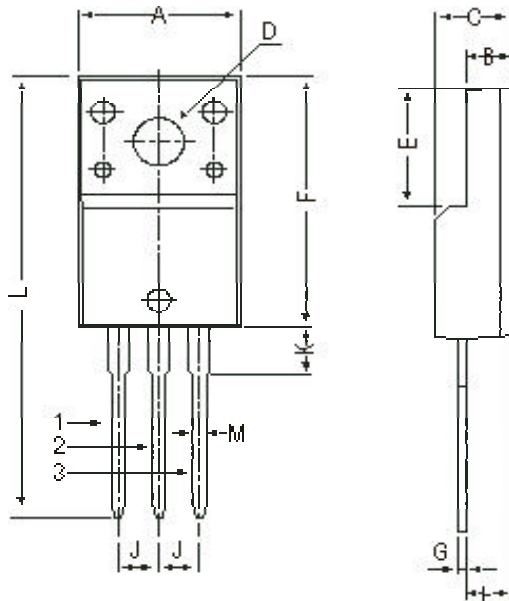
DYNAMIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Critical Rate of Rise of Off State Voltage	dV_D/dt	$V_{DM}=67\%$ $V_{DRM}=\text{max}$, $T_J=125^\circ\text{C}$, exponential waveform, gate open circuit	100			V/ μs
Critical Rate of Change of Commutating Voltage	dV_{com}/dt	$V_{DM}=400\text{V}$, $T_J=95^\circ\text{C}$, $I_{T(RMS)}=4\text{A}$, $dI_{com}/dt=1.8\text{A/ms}$, gate open circuit		50		V/ μs
Gate Controlled turn On time	t_{gt}	$I_{TM}=6\text{A}$, $V_D=V_{DRM} \text{ max}$, $I_G=0.1\text{A}$, $dI_G/dt=5\text{A}/\mu\text{s}$		2.0		μs

ISOLATION LIMITING VALUE and CHARACTERISTIC

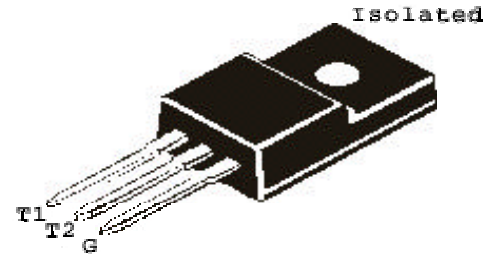
PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
R.M.S Isolation Voltage from all three terminals to external heatsink	V_{ISOL}	$f=50\text{-}60\text{ Hz}$; sinusoidal waveform; R.H. $\leq 65\%$; clean and dustfree			2500	V
Capacitance from T2 to external heatsink	C_{ISOL}	$f=1\text{MHz}$		10		pF

TO-220FP Fully Isolated Plastic Package



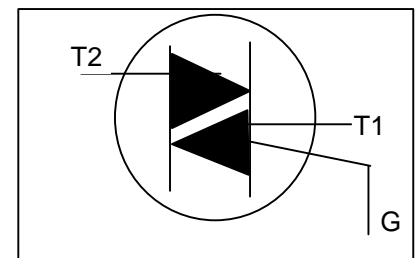
DIM	MIN	MAX
A	9.96	10.36
B	2.60	3.00
C	4.50	4.90
D	3.10	3.30
E	7.90	8.20
F	16.87	17.27
G	0.45	0.50
H	2.56	2.96
J	2.34	2.74
K	—	3.08
L	—	30.05
M	—	0.80

All dimensions in mm.

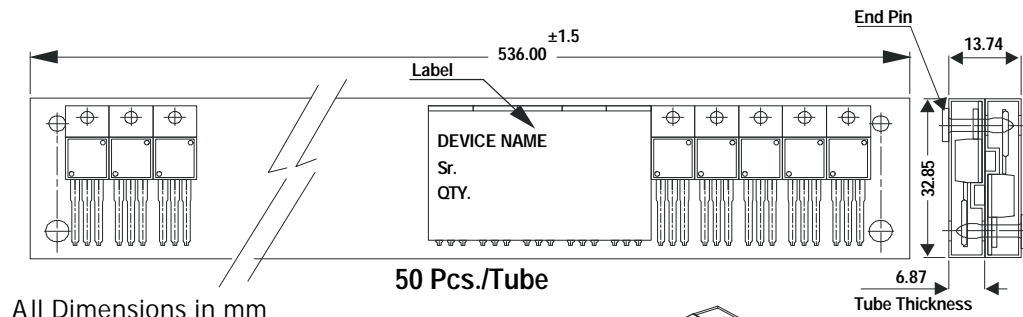


Pin Configuration

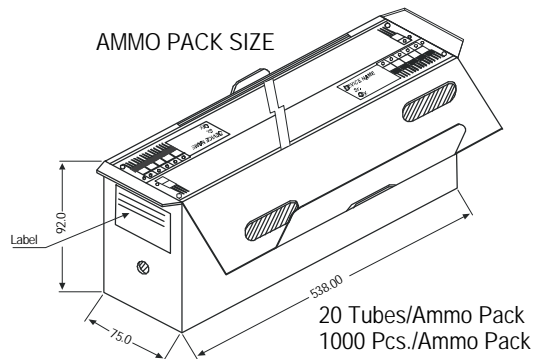
1. Main Terminal 1
 2. Main Terminal 2
 3. Gate
- Case Isolated



TO-220FP Tube Packing



AMMO PACK SIZE



Packing Details

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-220FP	200 pcs/polybag	396 gm/200 pcs	3" x 7.5" x 7.5"	1.0K	17" x 15" x 13.5"	16.0 K	36 kgs
	50 pcs/tube	120 gm/50 pcs	3.5" x 3.7" x 21.5"	1.0K	19" x 19" x 19"	10.0 K	29 kgs

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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