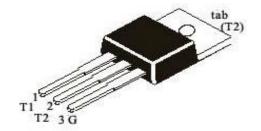


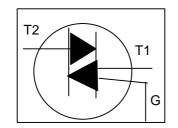
### Continental Device India Limited

An IS/ISO 9002 and IECQ Certified Manufacturer



TRIAC BT136





TO-220 Plastic Package

For use in high bidirectional transient and blocking voltage applications, and for high thermal cycling performance. Typical Application include Motor Control, Industrial and Domestic Lighting, Heating and Static Switching.

#### **ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	TEST CONDITION	CONDITION VALUE	
Repetitive Peak Off State Voltage	$^*V_{DRM}$		600	V
RMS on State Current	I <sub>T (RMS)</sub>	full sine wave, T <sub>mb</sub> ≤107°C	4.0	Α
Non Repetitive Peak on State Current	full sine wave, T <sub>J</sub> =25°C prior to Surge			
		t=20ms t=16.7ms	25 27	A A
I <sup>2</sup> t for Fusing	l <sup>2</sup> t	t=10ms	3.1	A <sup>2</sup> s
Repetitive Rate of Rise of on State Current After Triggering	dl <sub>⊤</sub> /dt	I <sub>TM</sub> =6A, I <sub>G</sub> =0.2A, dI <sub>G</sub> /dt=0.2A/μs T2+ G+ T2+ G- T2- G- T2- G+	50 50 50 10	A/μs A/μs A/μs A/μs
Peak Gate Current	$I_{GM}$		2.0	Α
Peak Gate Voltage	$V_{GM}$		5.0	V
Peak Gate Power	$P_{GM}$		5.0	W
Average Gate Power	P <sub>G (AV)</sub>	Over any 20ms period	0.5	W
Storage Temperature	$T_{stg}$		- 40 to 150	°C
Operating Junction Temperature	T <sub>j</sub>		125	°C

<sup>\*</sup>The rate of rise of current should not excees 3A/ms

### THERMAL RESISTANCE

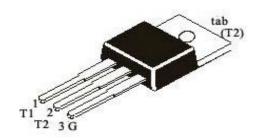
Junction to Mounting Base	R <sub>th (j-mb)</sub>	full cycle	3.0 max	K/W
		half cycle	3.7 max	K/W
Junction to Ambient (typical)	R <sub>th (j-a)</sub>	in free air	60 typ	K/W

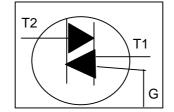
### ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C unless specified otherwise)

PARAMETER	SYMBOL	TEST CONDITION	MIN	MAX	UNIT		
Gate Trigger Current	I <sub>GT</sub>	$V_{D}=12V, I_{T}=0.1A$					
		T2+ G+		35	mΑ		
		T2+ G-		35	mΑ		
		T2- G-		35	mΑ		
		T2- G+		70	mΑ		

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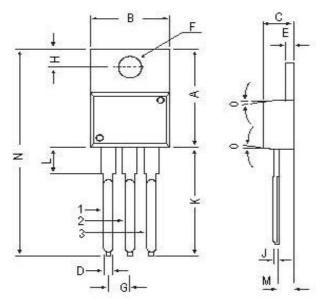
ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C unless specified otherwise)

PARAMETER	SYMBOL	L TEST CONDITION MIN		MAX	UNIT
Latching Current	Ι <sub>L</sub>	$I_L$ $V_D=12V, I_{GT}=0.1A$			
		T2+ G+		20	mΑ
		T2+ G-		30	mΑ
		T2- G-		20	mΑ
		T2- G+		30	mΑ
Holding Current	I <sub>H</sub>	$V_{D}=12V, I_{GT}=0.1A$		15	mΑ
On State Voltage	$V_{T}$	I <sub>T</sub> =5A		1.7	V
Gate Trigger Voltage	$V_{GT}$	$V_D = 12V, I_T = 0.1A$		1.5	V
		V <sub>D</sub> =400V, I <sub>T</sub> =0.1A,T <sub>J</sub> =125°C	0.25		V
Off State Leakage Current	I <sub>D</sub>	V <sub>D</sub> =max, V <sub>DRM</sub> =max, T <sub>J</sub> =125⁰C		0.5	mA

## **DYNAMIC CHARACTERISTICS**

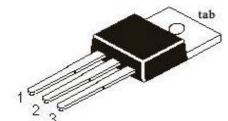
PARAMETER	SYMBOL TEST CONDITION		MIN	TYP	MAX	UNIT
Critical Rate of Rise of off State Voltage	d <sub>VD</sub> /dt	$V_{DM}$ =67% $V_{DRM}$ =max, $T_J$ =125°C, exponential waveform, gate open circuit	100			V/μs
Critical Rate of Change of Commutating Voltage	dV <sub>com</sub> /dt	V <sub>DM</sub> =400V, T <sub>J</sub> =95°C, I <sub>T(RMS)</sub> =4A, d/ <sub>com</sub> /dt=1.8A/ms, gate open circuit		50		V/μs
Gate Controlled turn on time	<b>t</b> gt	$I_{TM}$ =6A, $V_D$ = $V_{DRM}$ max, $I_G$ =0.1A, $dI_G$ / $dt$ =5A/ $\mu$ s		2.0		μs

# **TO-220 Plastic Package**



DIM	MIN	MAX		
Α	14.42	16.51		
В	9.63	10.67		
С	3.56	4.83		
D	<u> </u>	0.90		
Е	1.15	1.40		
F	3.75	3.88		
G	2.29	2.79		
Н	2.54	3,43		
J	<del>-</del>	0.56		
К	12.70	14.73		
L	2.80	4.07		
M	2.03	2.92		
N		31.24		
0	7 DEG			

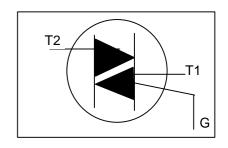
All diminsions in mm.



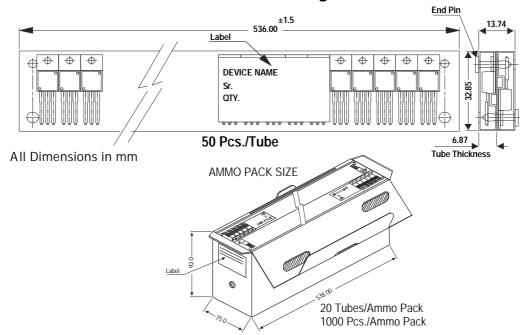
Pin Configuration

- 1. Main Terminal 1
- -2, Main Terminal 2
- 3. Gate

tab Main Terminal 2



## **TO-220 Tube Packing**



# Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	GrWt
TO-220 /FP	200 pcs/polybag	396 gm/200 pcs	3" x 7.5" x 7.5"	1.OK	17" x 15" x 13.5"	16.0K	36 kgs
	50 pcs/tube	120 gm/50 pcs	3.5" x 3.7" x 21.5"	1.0K	19" x 19" x 19"	10.0K	29 kgs

Notes BT136

TO-220
Plastic Package

#### **Disclaimer**

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