

Type	Ordering code	Package
TDA 5800	Q67000-A1777	DIP 22

The TDA 5800 contains a 4-stage broadband amplifier with controllable gain, a limiter, a synchronous demodulator for AM, an FM demodulator for generating the AFC voltage, and an AGC generator for the IF amplifier and tuner.

An external PNP transistor is required for a VTR connection according to the IEC standard.

Features

- Suitable for standard VTR connection
- Switchable AFC
- Fast control
- Positive and negative video output

Maximum ratings

Supply voltage	V_S	16.5	V
Junction temperature	T_j	150	°C
Storage temperature range	T_{slg}	-40 to 125	°C
Thermal resistance (system-air)	R_{thSA}	70	K/W

Operating range

Supply voltage	V_S	10 to 15.8	V
IF frequency	f_{IF}	15 to 75	MHz
Ambient temperature	T_A	0 to 70	°C

Characteristics

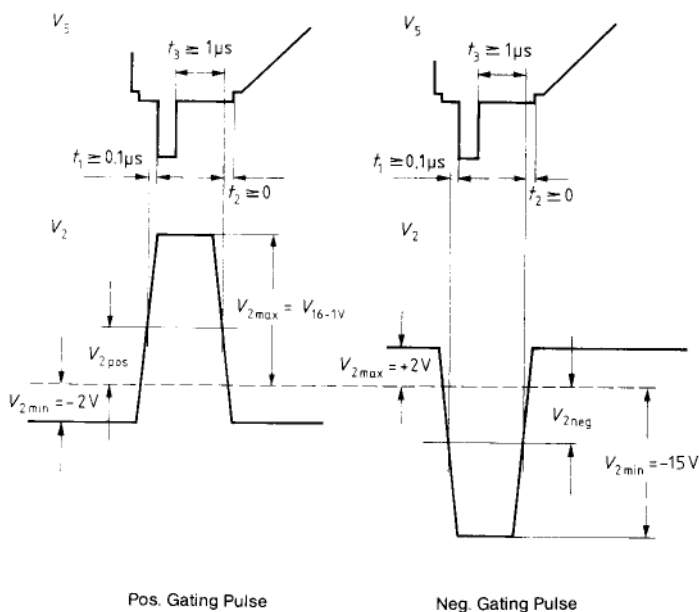
 $V_S = 13 \text{ V}; T_A = 25^\circ \text{C}$

	min	typ	max	
Current consumption		60		mA
Stab. reference voltage		6.0		Vdc
Tuner control current		4.0		mA
Tuner AGC threshold	3.0		5.0	Vdc
Gating pulse voltage	0		$V_{16}-1$	V
	-2.0	3.0		V
	-15.0	-3.0	2.0	V
Input voltage at $G_{\max}$ ($V_{5pp} = 3 \text{ V}$)			100	μV
AGC range		60		dB
IF control voltage for $G_{\max}$	0			Vdc
IF control voltage for $G_{\min}$			5.0	Vdc
IF control voltage for VTR switchover	8.0		V_{16}	Vdc
AFC output current		± 1.0		mA
AFC switch OFF ($V_{11} = V_{12}; R = 10 \text{ k}\Omega$)	0		4.0	Vdc
AFC switch ON ($V_{11} = V_{12}; R = \infty$)		5.3		Vdc
AFC characteristics ($di/df > 0$)	3.0		V_{16}	Vdc
AFC characteristics ($di/df < 0$)	0		1.0	Vdc
Video output voltage pos. ($R_L = \infty$)		3.0		Vdc
Sync pulse level		2.0		Vdc
DC voltage ($V_3 = 5 \text{ V}; V_{22/21} = 0$)		5.3		Vdc
Output current to ground across R		-5.0		mA
Output current (to +)		2.0		mA
Output resistance		150		Ω
Video output voltage neg. ($R_L = \infty$)		3.0		V
Sync pulse level		$V_{16}-2$		V
DC voltage ($V_3 = 5 \text{ V}; V_{22/21} = 0$)		$V_{16}-5.3$		V
Output current to ground across R		-5.0		mA
Output current (to +)		1.0		mA
VTR output voltage neg.		1.0		V
$R_L = \infty$; VTR recording				
Sync pulse level	$V_{7/1}$	$V_{16}-1.2$		Vdc
$R_L = \infty$; VTR recording				
DC voltage	$V_{7/1}$	$V_{16}-2.3$		Vdc
$V_3 = 5 \text{ V}; V_{22/21} = 0$				
DC voltage	$V_{7/1}$	$V_{16}-0.9$		Vdc
$V_3 \geq 8 \text{ V}$; VTR playback				
Output current	I_{q7}	-5.0		mA
to ground across R				
Output current (to +)	I_{q7}	1.0		mA
Video amplifier VTR playback	V	3.0		
$V = V_5/V_3; V_3 = 1 \text{ V}_{pp}$				

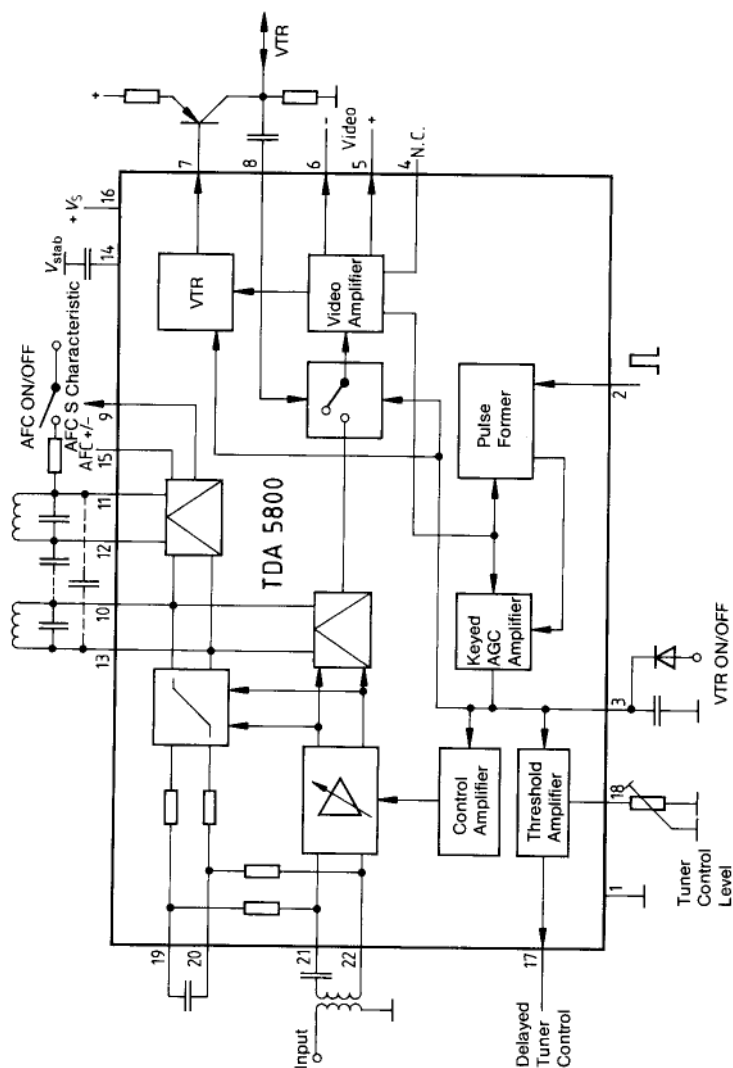
Additional application data
 (not measured)

Input impedance
 Output impedance
 AFC input impedance
 Output resistance
 Output resistance
 Residual IF (basic frequency)
 Video bandwidth (−3 dB) VTR recording
 Video bandwidth
 (VTR recording $V_{8pp} = 1$ V)
 Intermodulation ratio with
 reference to f_{CC}
 (sound-color-beat frequency)

	min	typ	max	
$Z_{i22/21}$		1.8/2		kΩ/pF
$Z_{q10/13}$		6.6/2		kΩ/pF
$Z_{i11/12}$		20		kΩ
R_{q6}		150		Ω
R_{q7}		150		Ω
V_5, V_6		10		mV
B_{video}		6.0		MHz
B_{video}		10.0		MHz
a		45		dB

Pulse diagram


Block diagram and measurement circuit



Application circuit

